MITS

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE

(Deemed to be University under section 3 of UGC Act, 1956)

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CIRCULAR

Date: 13.11.2026
Madanapalle

All the **B.Tech. II Year I Semester (R25)** Regular students are hereby informed that the Model Lab Test will be conducted as per the time table given below.

TIME TABLE (Model Lab Test)

Dept. of Electrical and Electronics Engineering

(2025 Admitted Batch)

Section	Course Name & Code	Date	Timings	Roll No.S	Venue	No.of Students
A	DC Machines and Transformers Laboratory - 25BAEEELC02	16.11.2026	02.10 PM to 04.10 PM	25MRA02001 - 2008, 25MRA02010 - 2011, 26MLA02001 - 2026	SRB 012C	36
				Total No.of Students		**36**

Section	Course Name & Code	Date	Timings	Roll No.S	Venue	No.of Students
A	Analog Electronics Circuits Laboratory - 25BAEEELC03	20.11.2026	01.10 PM to 03.10 PM	25MRA02001 - 2008, 25MRA02010 - 2011, 26MLA02001 - 2026	SRB 119	36
				Total No.of Students		**36**

Section	Course Name & Code	Date	Timings	Roll No.S	Venue	No.of Students
A	Digital Systems Laboratory - 25BAEEELC04	19.11.2026	01.10 PM to 03.10 PM	25MRA02001 - 2008, 25MRA02010 - 2011, 26MLA02001 - 2026	SRB 119	36
				Total No.of Students		**36**

Controller of Examinations

CONTROLLER OF EXAMINATIONS
Madanapalle Institute of Technology & Science
(Deemed to be University)
MADANAPALLE-517 325, A.P.